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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/534,164	05/05/2005	Matthias Muth	DE02 0252 US	9960	
65913 7590 027222011 NXP, B.V. NXP INTELLECTUAL PROPERTY & LICENSING			EXAMINER		
			ZAMAN, FAISAL M		
M/S41-SJ 1109 MCKAY	Y DRIVE		ART UNIT	PAPER NUMBER	
SAN JOSE, CA 95131			2111		
			NOTIFICATION DATE	DELIVERY MODE	
			02/22/2011	ELECTRONIC	

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail $\,$ address(es):

ip.department.us@nxp.com

Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)	
10/534,164	MUTH, MATTHIAS	
Examiner	Art Unit	
Faisal M. Zaman	2111	

	Faisai M. Zaman	2111	
The MAILING DATE of this communication appe	ars on the cover sheet with the c	orrespondence add	ress
THE REPLY FILED 09 February 2011 FAILS TO PLACE THIS	APPLICATION IN CONDITION FO	R ALLOWANCE.	
 X The reply was filed after a final rejection, but prior to or on application, applicant must timely file one of the following application in condition for allowance; (2) a Notice of Appe for Continued Examination (RCE) in compliance with 37 C periods: 	eplies: (1) an amendment, affidavit al (with appeal fee) in compliance	, or other evidence, whith 37 CFR 41.31; or	hich places the (3) a Request
The period for reply expiresmonths from the mailing	date of the final rejection.		
b) The period for reply expires on: (1) the mailing date of this Ar no event, however, will the statutory period for reply expire la	dvisory Action, or (2) the date set forth i ter than SIX MONTHS from the mailing	date of the final rejection	n.
Examiner Note: If box 1 is checked, check either box (a) or (I MONTHS OF THE FINAL REJECTION, See MPEP 706.07(f		FIRST REPLY WAS FI	ED WITHIN TWO
Extensions of time may be obtained under 37 CFR 1.138(a). The date whave been filled is the date for purposes of determining the period of valued x7 CFR 1.17(a) is calculated from: (1) the expiration date of the set forth in (b) above, if checked. Any reply received by the Office later may reduce any earned patient term adjustment. See 37 CFR 1.704(b). NOTICE OF APPEAL.	ension and the corresponding amount of hortened statutory period for reply origin	of the fee. The appropria nally set in the final Office	ite extension fee e action; or (2) as
 The Notice of Appeal was filed on A brief in compi filing the Notice of Appeal (37 CFR 41.37(a)), or any exter Notice of Appeal has been filed, any reply must be filed with AND NOTICE of Appeal has been filed, any reply must be filed with AND NOTICE of Appeal has been filed. 	sion thereof (37 CFR 41.37(e)), to	avoid dismissal of the	
<u>AMENDMENTS</u>			
 The proposed amendment(s) filed after a final rejection, be (a) They raise new issues that would require further core (b) They raise the issue of new matter (see NOTE below 	sideration and/or search (see NOT		cause
 They are not deemed to place the application in bett appeal; and/or 	er form for appeal by materially rec	lucing or simplifying the	ne issues for
(d) They present additional claims without canceling a c	orresponding number of finally reje	cted claims.	
NOTE: (See 37 CFR 1.116 and 41.33(a)).			
 4. The amendments are not in compliance with 37 CFR 1.12 5. Applicant's reply has overcome the following rejection(s): 		npliant Amendment (PTOL-324).
Newly proposed or amended claim(s) would be all non-allowable claim(s).		imely filed amendmer	t canceling the
 For purposes of appeal, the proposed amendment(s): a) [how the new or amended claims would be rejected is prov 		be entered and an ex	planation of
The status of the claim(s) is (or will be) as follows: Claim(s) allowed:			
Claim(s) objected to:			
Claim(s) rejected:			
Claim(s) withdrawn from consideration: AFFIDAVIT OR OTHER EVIDENCE			
8. The affidavit or other evidence filed after a final action, but	before or on the date of filing a No	tice of Appeal will not	be entered
because applicant failed to provide a showing of good and was not earlier presented. See 37 CFR 1.116(e).	sufficient reasons why the affidavi	t or other evidence is	necessary and
 The affidavit or other evidence filed after the date of filing entered because the affidavit or other evidence failed to or showing a good and sufficient reasons why it is necessary 	vercome <u>all</u> rejections under appea	l and/or appellant fail:	to provide a
10. The affidavit or other evidence is entered. An explanation REQUEST FOR RECONSIDERATION/OTHER	of the status of the claims after en	try is below or attach	ed.
The request for reconsideration has been considered but See Continuation Sheet.	does NOT place the application in	condition for allowan	ce because:
12. Note the attached Information <i>Disclosure Statement(s)</i> . (13. Other:	PTO/SB/08) Paper No(s)		
- —			
	/Faisal M Zaman/ Primary Examiner, Art U	nit 2111	

Continuation of 11. does NOT place the application in condition for allowance because: AAPA, Feuerstraeter, Bongiorno, and Werle teach all of the limitations of the claims, as discussed in the Final Office action,

Regarding Claims 1 and 6, Applicant argues that the provided motivation does not provide "further explanation or mention of any application which the so-called simplification would supposedly provide any specific benefit to any LIN-directed implementation", (Response, page 5, fourth paragraph). The examiner disagrees. Contrary to Applicant's argument, the Final Office action did in fact provide a reason as to why there would be a simplification of the AAPA system (i.e., "for the purpose of simplifying the ultimate system design; i.e., so that circuit designers could specify one integrated circuit rather than having to combine several circuits"). To reterate the entire point of the 35 USC 103(a) combination, AAPA states that all of the claimed components were known in the prior at (see, e.g., page 2, lines 11-14 and lines 23-28). The purpose of the combination was to show that providing all of the claimed components into a single integrated circuit was known in the art (as taught by Feuerstraeter). Accordingly, it would have been obvious to one of ordinary skill in the current rejection (i.e., AAPA in view of Feuerstraeter) has previously been affirmed by the Board (see Board decision of 1/12/2010). The only reason why the decision on reconsideration of 6/2/2010 was granted was because the Board had determined that the rejection of AAPA in view of Feuerstraeter (as presented in the Board decision of 1/12/2010) was a new ground of rejection (i.e., because the original rejection also relied upon the reference Ishikum) and Applicant was not given a fair chance to response to that specific ground of rejection.

Regarding Claims 2 and 3, Applicant argues that the provided motivation is "conclusory and uses circular reasoning" and the "AAPA reference would already have a more stable oscillator in the form of a clock driver circuit". (Response, page 6, first and third paragraphs). However, AAPA does not specify how the clock is generated, and therefore it is unclear as to how Applicant believes such a clock is more stable than an RO cosillator. Nevertheless, even if it was assumed that the combination of AAPA with Feuerthet raught a clock source which had a higher stability than an RO cosillator, the advantages of RO cosillators over such other clock sources (e.g., crystal oscillators) are known in the art. For example, RO cosillators are easier to implement and can be provided at a lower cost in addition, RO coscillators have a shorter start-up time compared to crystal oscillators. Accordingly, although it is believe that the motivation that was previously used was adequate, these additional motivations further show that the combination would have been obvious to or officiancy skill in the art.

Regarding Claims 4 and 5, Applicant argues that the motivation used in the combination was improper because "there is nothing apparent from the record that would raise a need to reduce latency particularly given the AAPA reference's rate-synchronous slave nodes' (Response, page 6, fourth and fifth paragraphs). To further clarify the examiner's position, the examiner was assuming the situation in which the incoming data stream was produced at a rate that was much lower than what the receiving device was able to process. By buffering the data and only receiving complete messages (rather than individual bytes), the receiving device would be able to reduce its latency in receiving the data. Additional advantages of buffering are also well known in the art. For example, in the event that a device cannot process the incoming data stream at the rate at which it is input, buffering allows the data to be stored rather than discarded so that the receiving device an process it at its own will. AAPA teaches that slave nodes having to receive data asynchronously (i.e., at different clock rates) was a known problem (see page 1, lines 7-12), and therefore the advantages of buffering vould apoly.

Therefore, the claims stand as previously rejected.